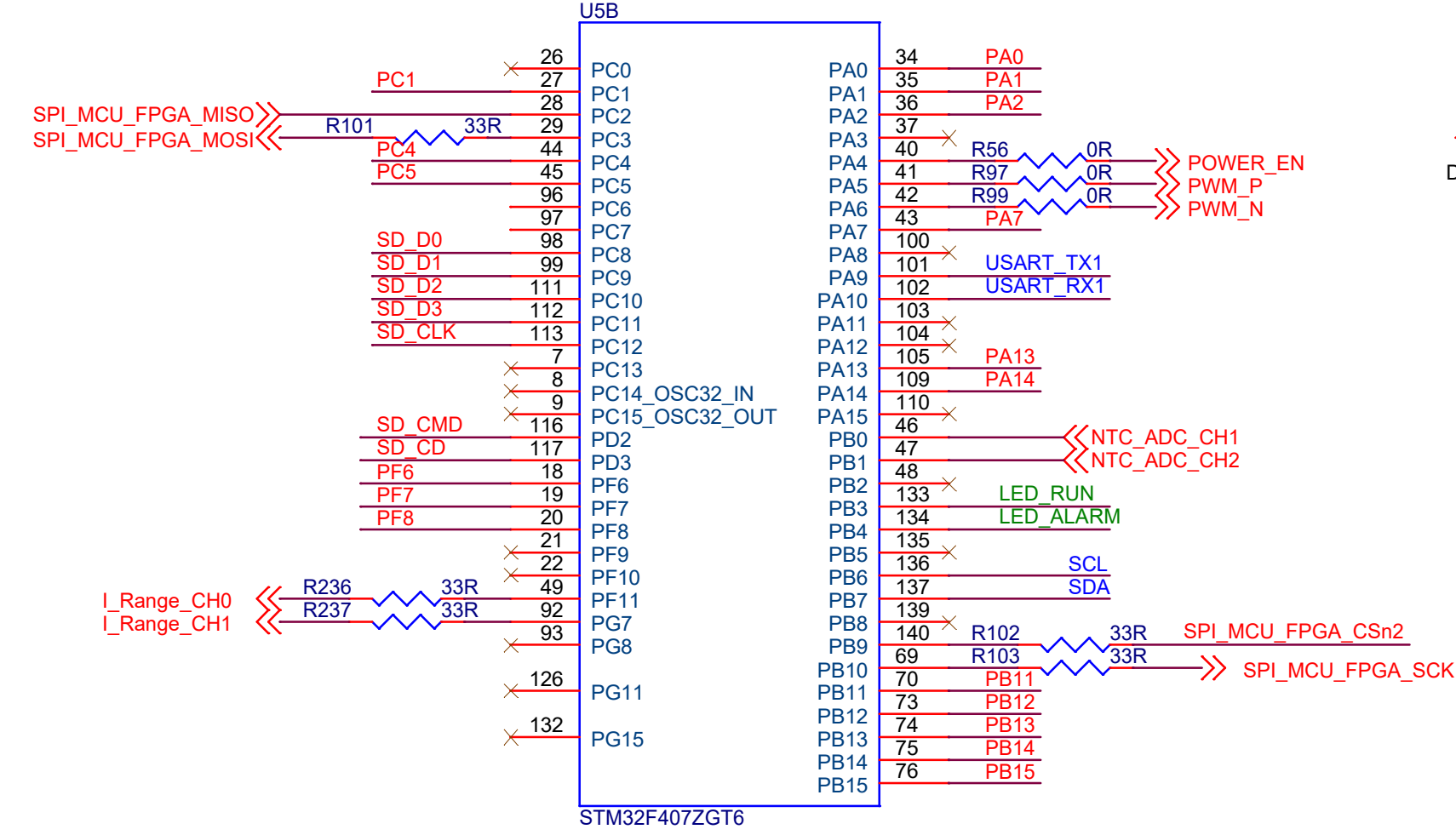
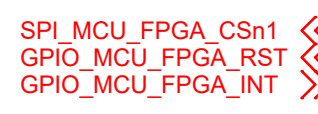
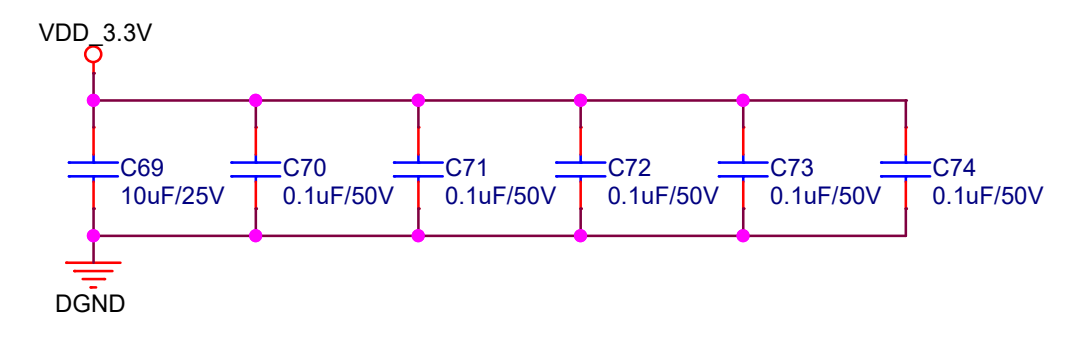
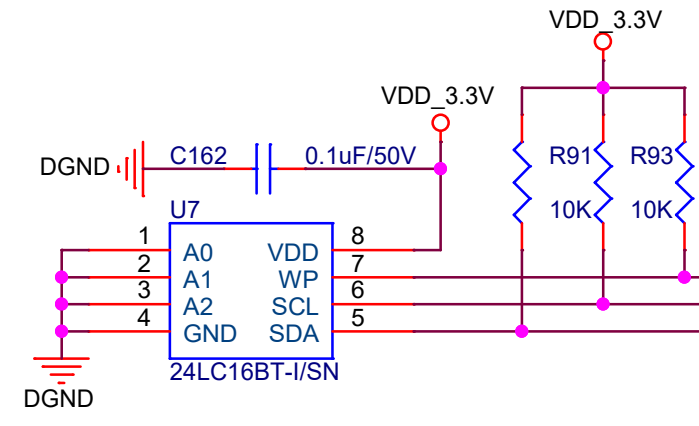


USBA		USBB		USBC	
EBUS_D15	79	PD10/FSMC_D15	PG14	129	R196
EBUS_D14	78	PD9/FSMC_D14	PG13	128	33R
EBUS_D13	77	PD8/FSMC_D13	PE6	5	EBUS_SCK
EBUS_D12	68	PE15/FSMC_D12	PE4	3	
EBUS_D11	67	PE14/FSMC_D11	PE3	2	
EBUS_D10	66	PE13/FSMC_D10	PE2	1	GPIO_MCU_GPIO_0
EBUS_D9	64	PE12/FSMC_D9	PD12	82	GPIO_MCU_GPIO_1
EBUS_D8	63	PE11/FSMC_D8	PD11	81	
EBUS_D7	60	PE9/FSMC_D7	PD10	80	
EBUS_D6	59	PE8/FSMC_D6	PG4	89	
EBUS_D5	58	PE7/FSMC_D4	PG3	88	
EBUS_D4	115	PD1/FSMC_D3	PG2	87	
EBUS_D3	114	PD0/FSMC_D2	PG1	56	EBUS_A12
EBUS_D2	86	PD5/FSMC_D1	PG0	55	EBUS_A11
EBUS_D1	85	PD14/FSMC_D0	PF15	54	EBUS_A10
EBUS_D0	118	PD4/FSMC_N0E	PF14	53	EBUS_A9
EBUS_N0E	122	PD6/FSMC_NWE	PF13	50	EBUS_A8
EBUS_NWE	123	PD7/FSMC_NWAIT	PF12	50	EBUS_A7
	142	PD7/FSMC_NCE2	PF5	15	EBUS_A6
	91	PE0/FSMC_NBL0	PF4	13	EBUS_A5
R100	33R	PE1/FSMC_NBL1	PF3	12	EBUS_A4
R187	33R	PG6/FSMC_INT2	PF2	11	EBUS_A3
R205	33R	PG9/FSMC_NE2	PF1	10	EBUS_A2
	127	PG10/FSMC_NE3	PF0	9	EBUS_A1
EBUS_NCS	127	PG12/FSMC_NE4			EBUS_A0

[illegible]

The schematic diagram illustrates the SPI interface between the SPI_MCU and the FPGA. The SPI_MCU is connected to the FPGA via pins R233, R234, and R73. The FPGA is connected to the SPI_MCU via pins R72, R218, and R230. The SPI_MCU is also connected to VDD_3.3V and GND. The FPGA is connected to VDD_3.3V and GND. The SPI_MCU is also connected to VDD_3.3V and GND. The FPGA is connected to VDD_3.3V and GND.



VDD 3.3V

D9 LED Green R60 1K PB14

D10 LED Red R61 1K PB15

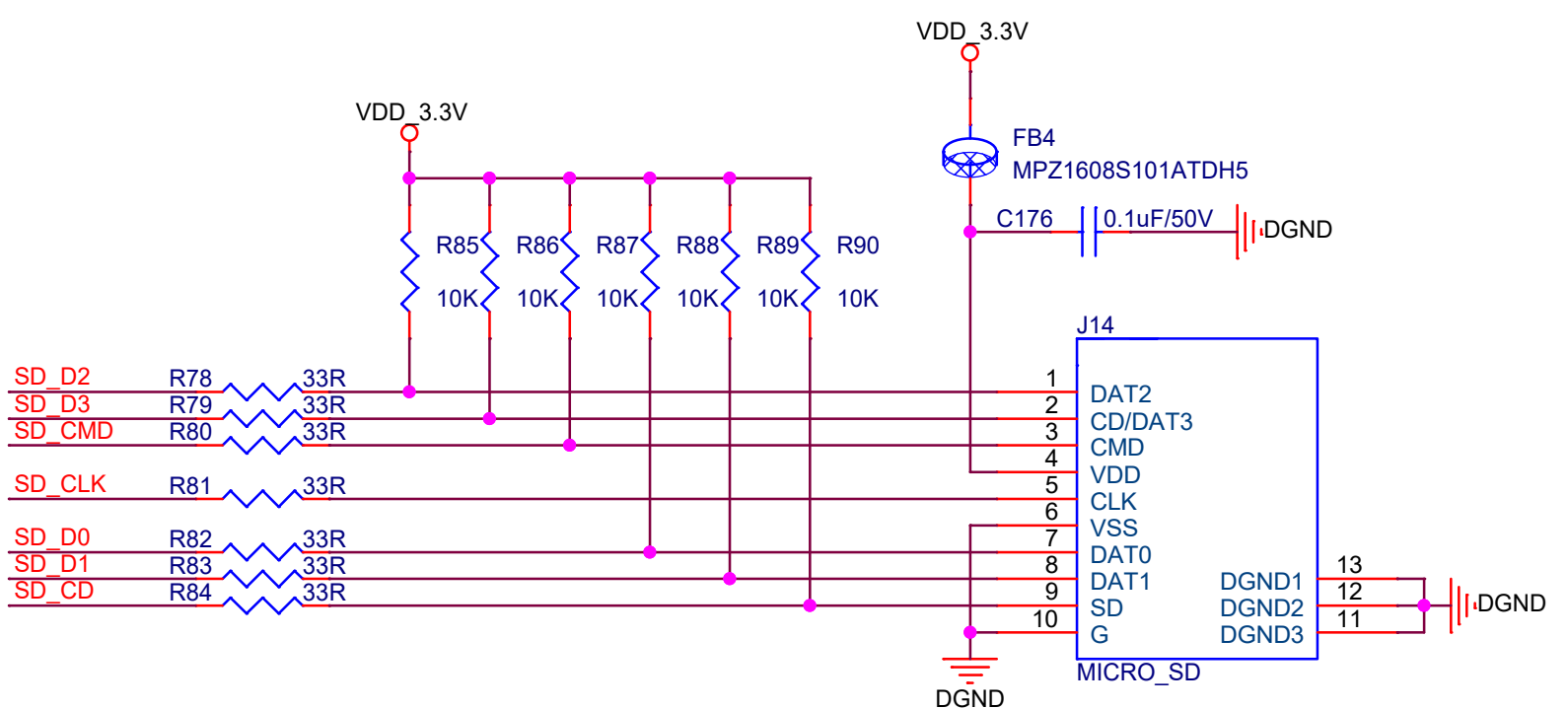
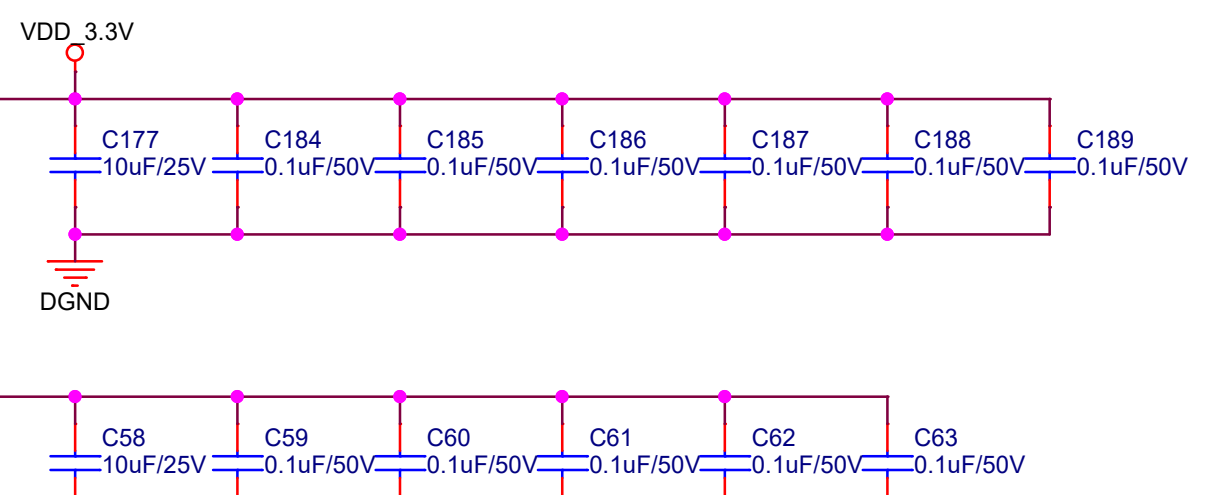
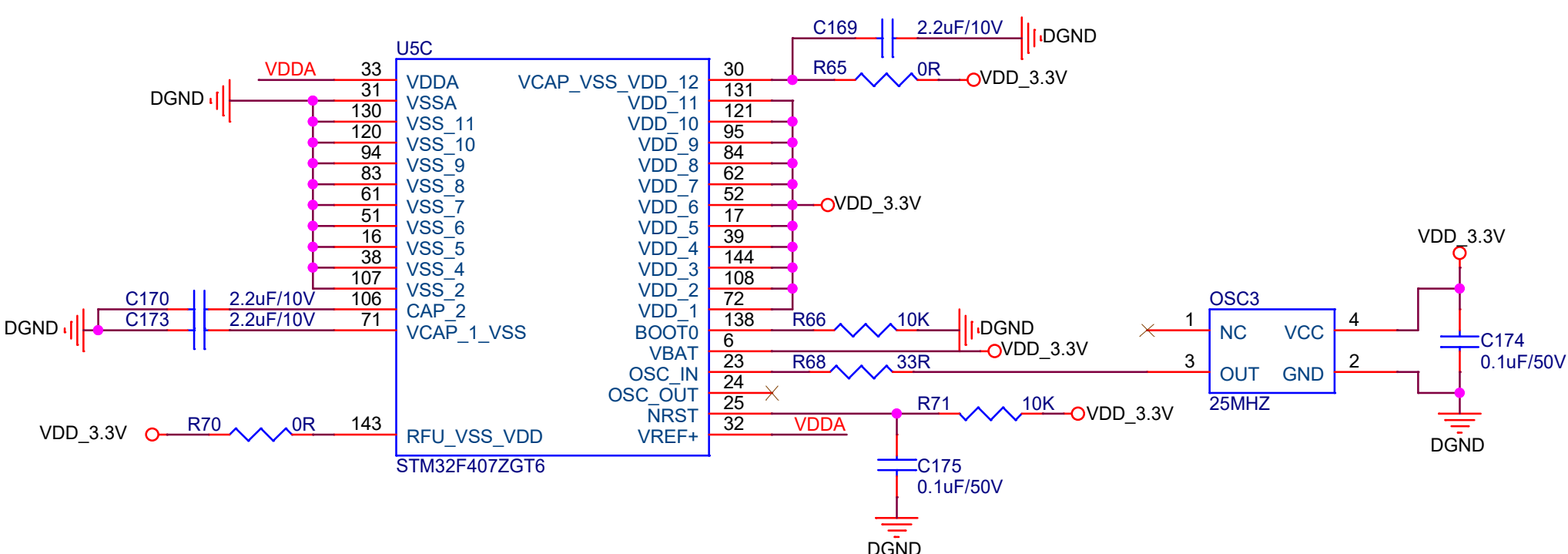
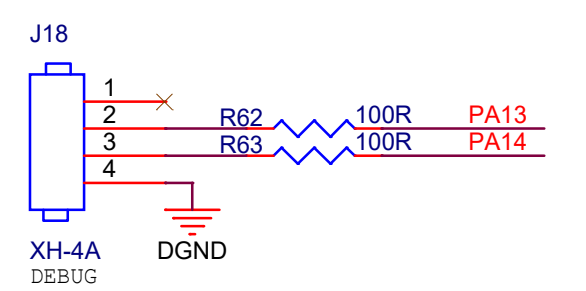


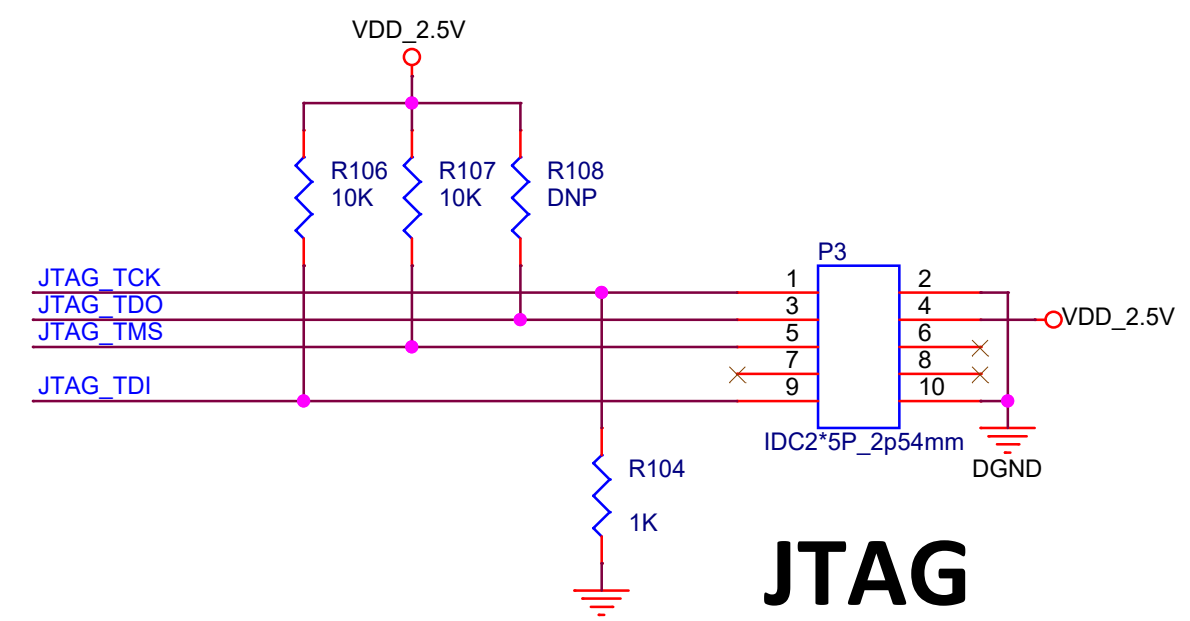
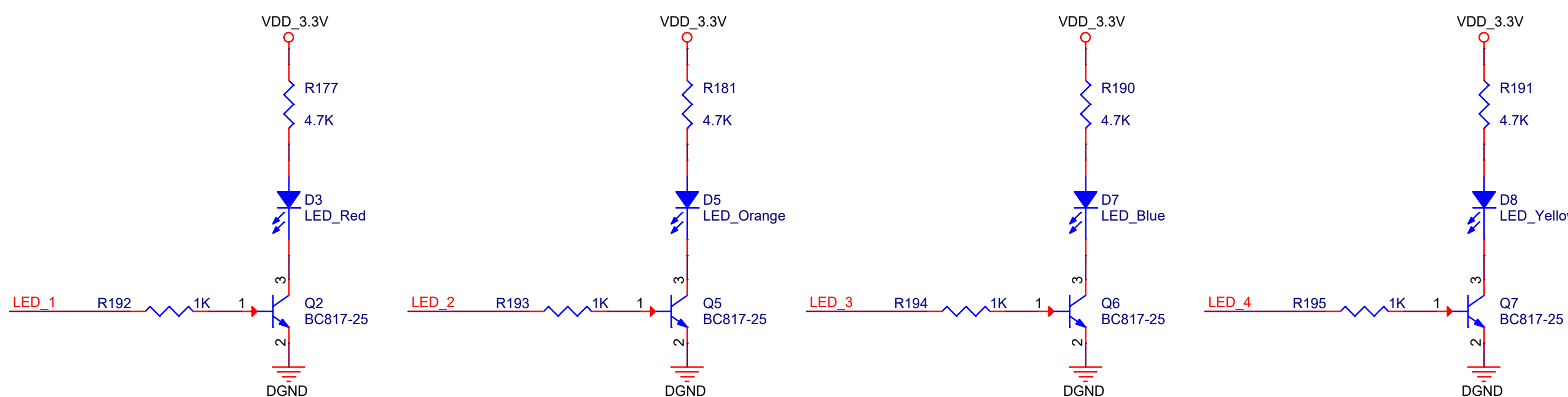
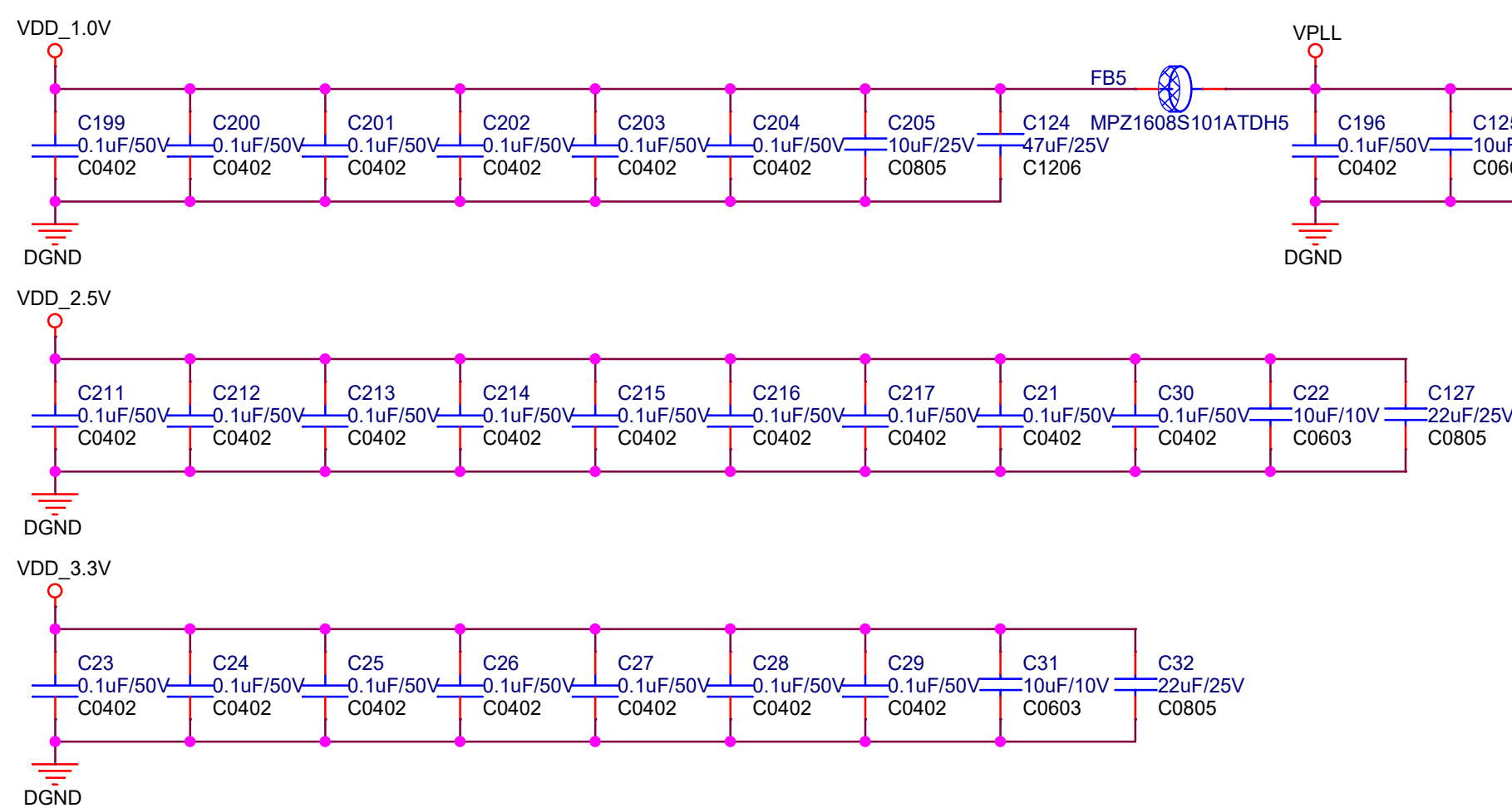
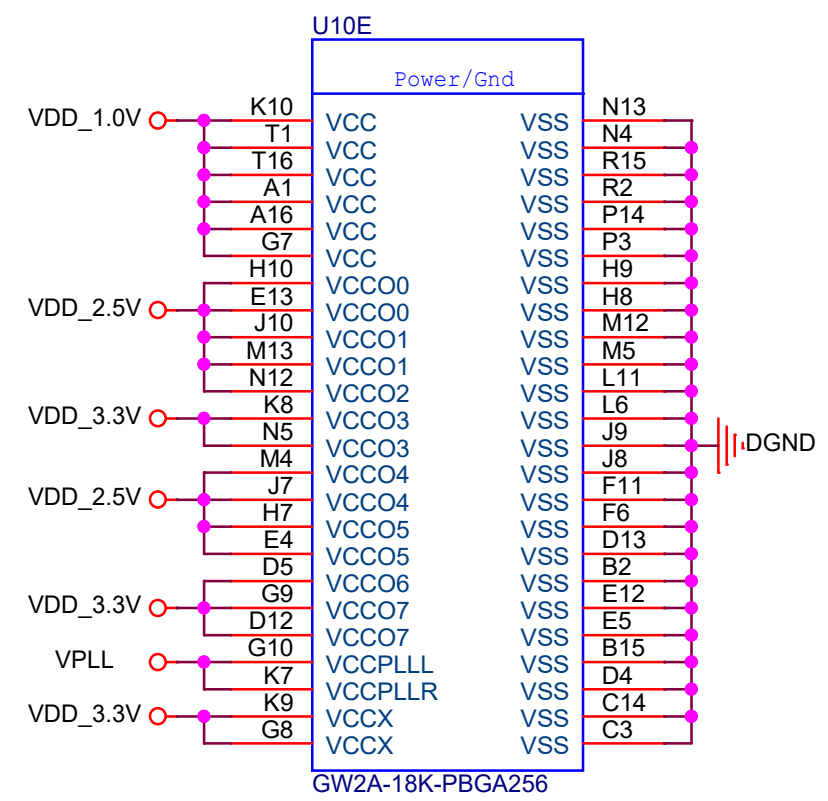
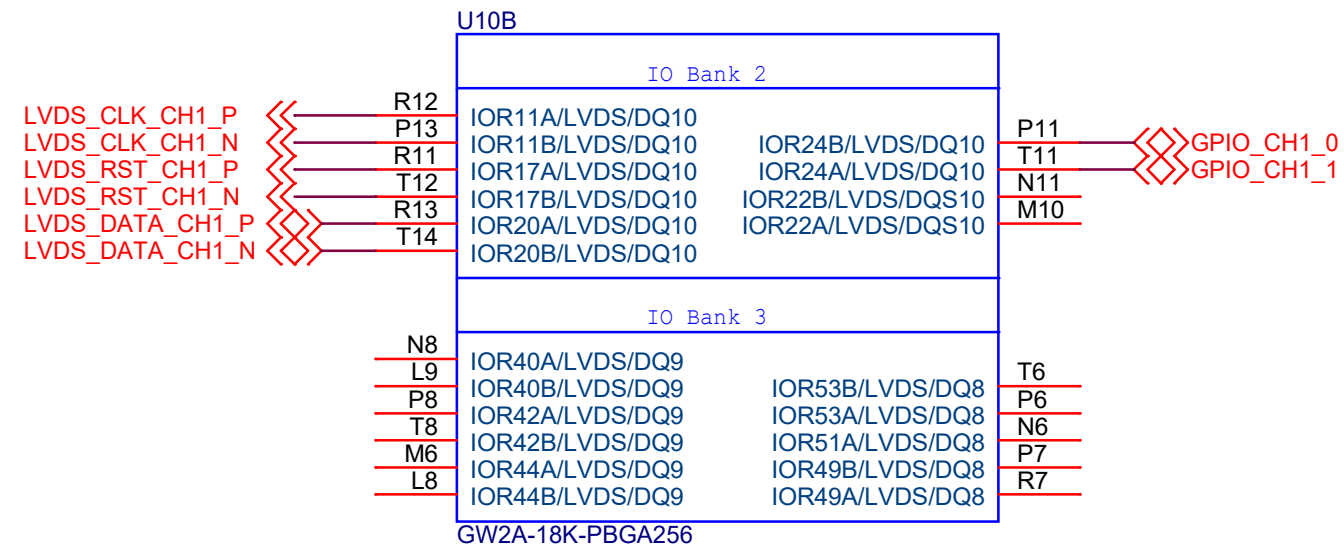
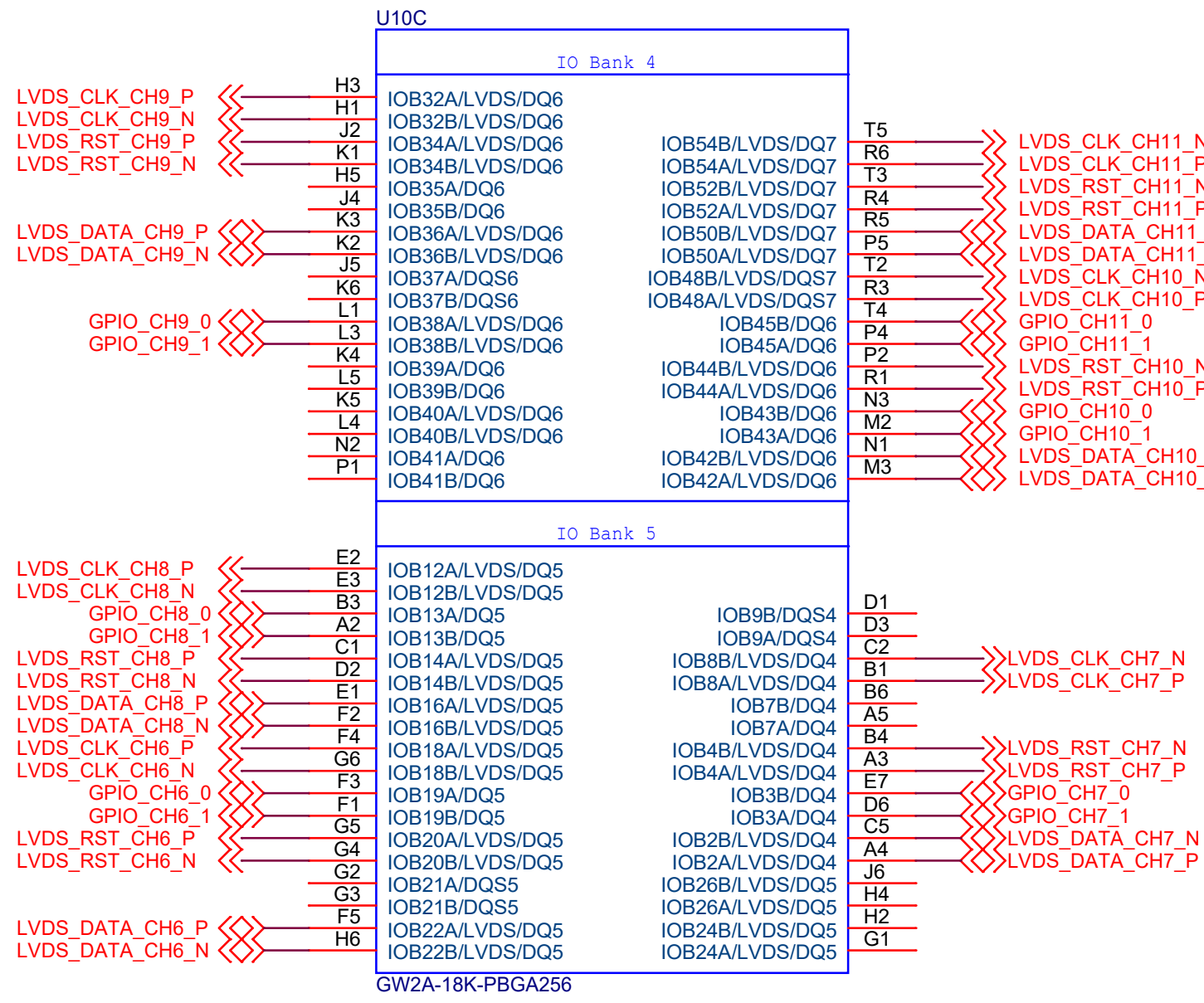
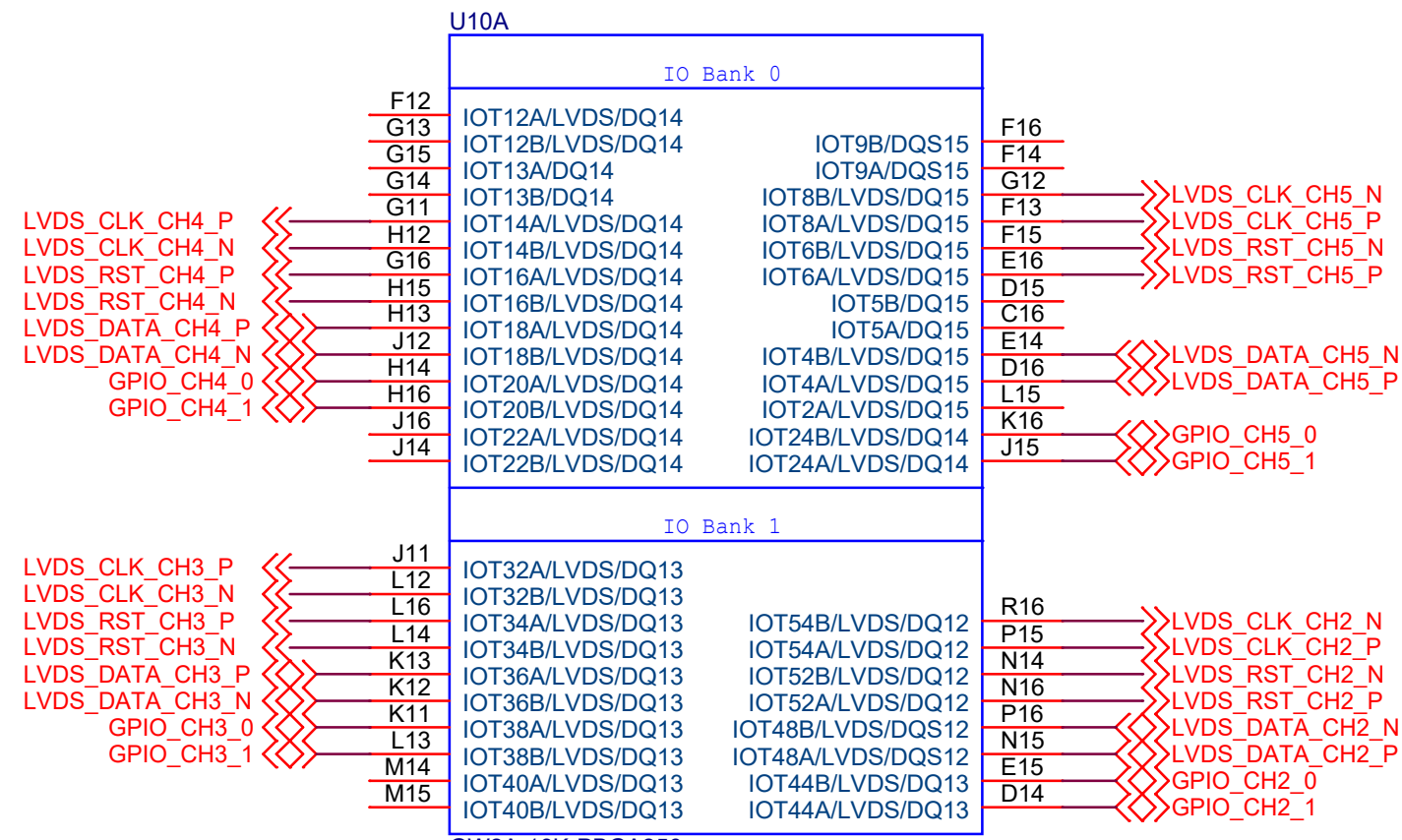
Diagram showing the pin connections for the IDC2*13P_2p54mm connector. The pins are numbered 1 to 26. The connections are as follows:

- Pin 1: VDD_3.3V
- Pin 2: LED_RUN
- Pin 3: LED_ALARM
- Pin 4: LED_RUN
- Pin 5: DGND
- Pin 6: LED_RUN
- Pin 7: USART_TX1
- Pin 8: DGND
- Pin 9: USART_RX1
- Pin 10: LED_STA
- Pin 11: LED_STA
- Pin 12: LED_STA
- Pin 13: LED_STA
- Pin 14: LED_LNK
- Pin 15: LED_LNK
- Pin 16: LED_LNK
- Pin 17: JTAG_TDI_TOP
- Pin 18: JTAG_TMS_TOP
- Pin 19: JTAG_TCK_TOP
- Pin 20: SCL
- Pin 21: SCL
- Pin 22: SDA
- Pin 23: FE_RX_P
- Pin 24: FE_TX_P
- Pin 25: FE_RX_N
- Pin 26: FE_TX_N

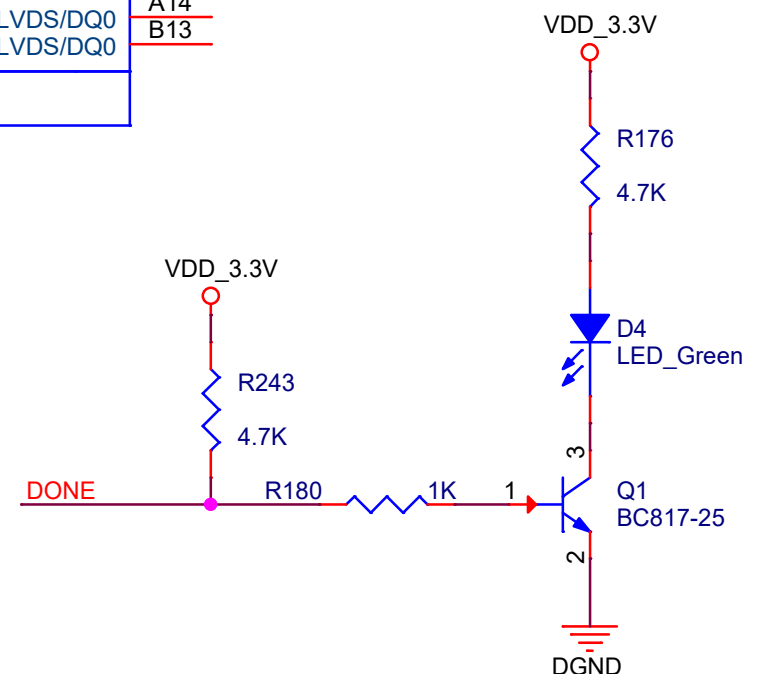
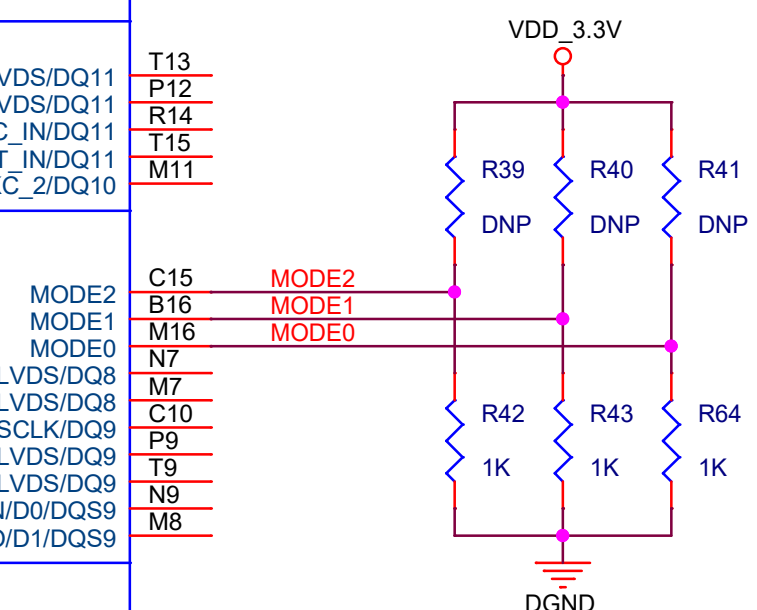
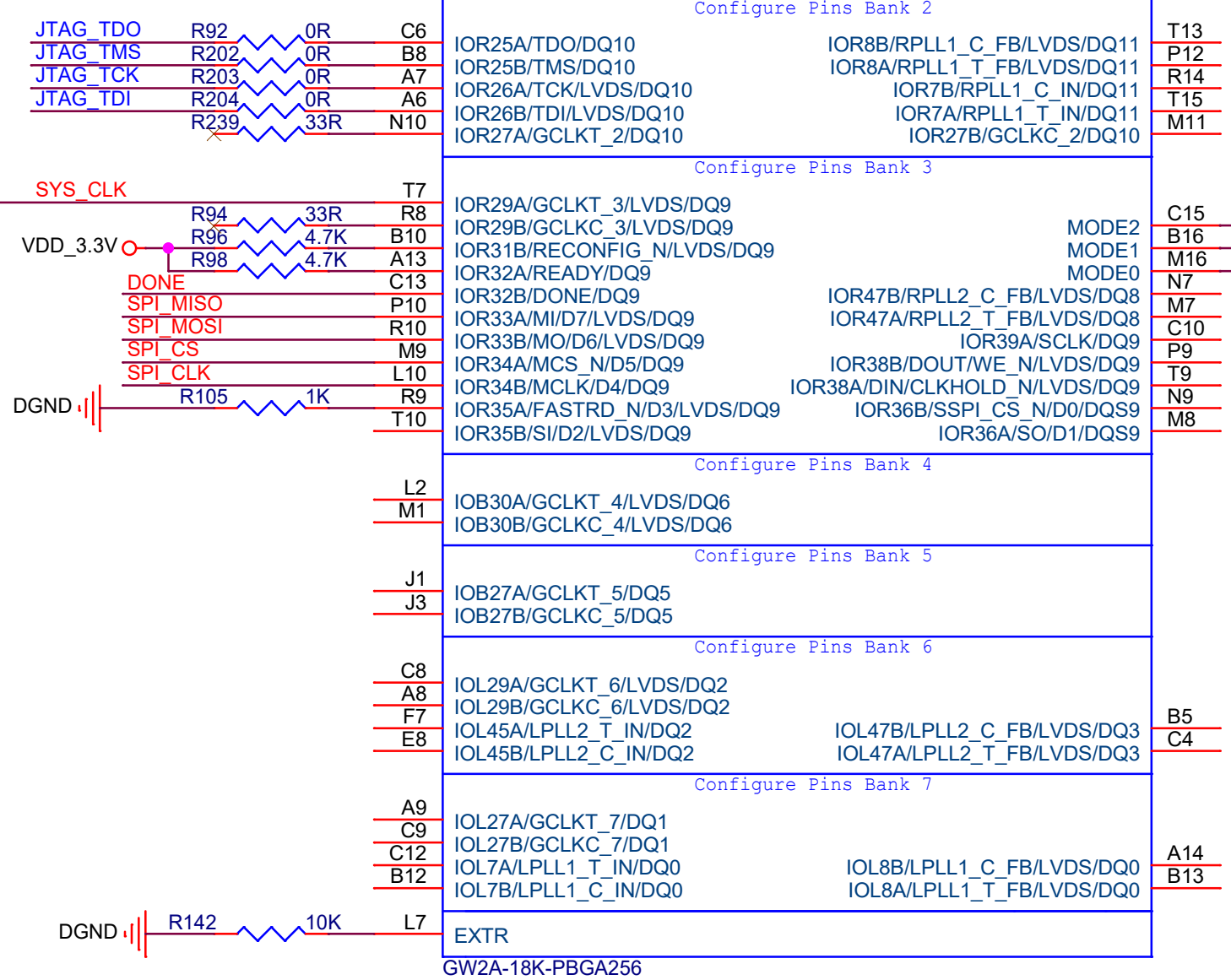
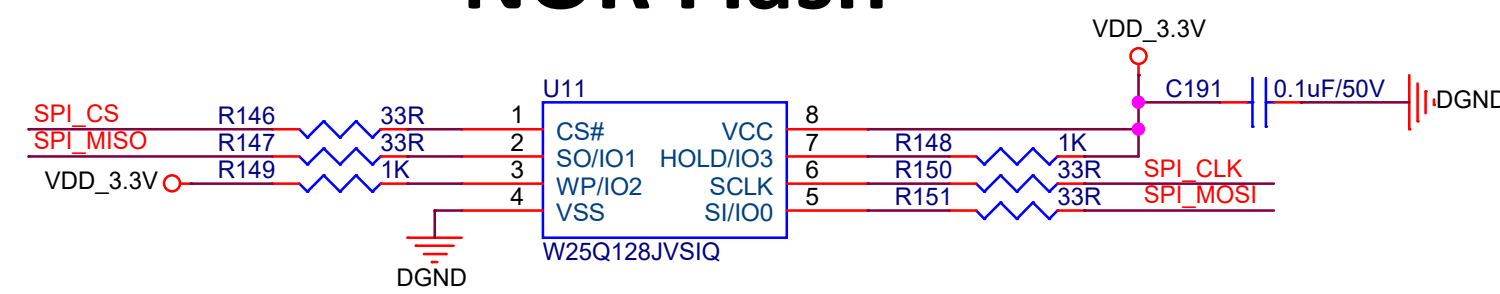
The diagram also shows the following labels for the pins:

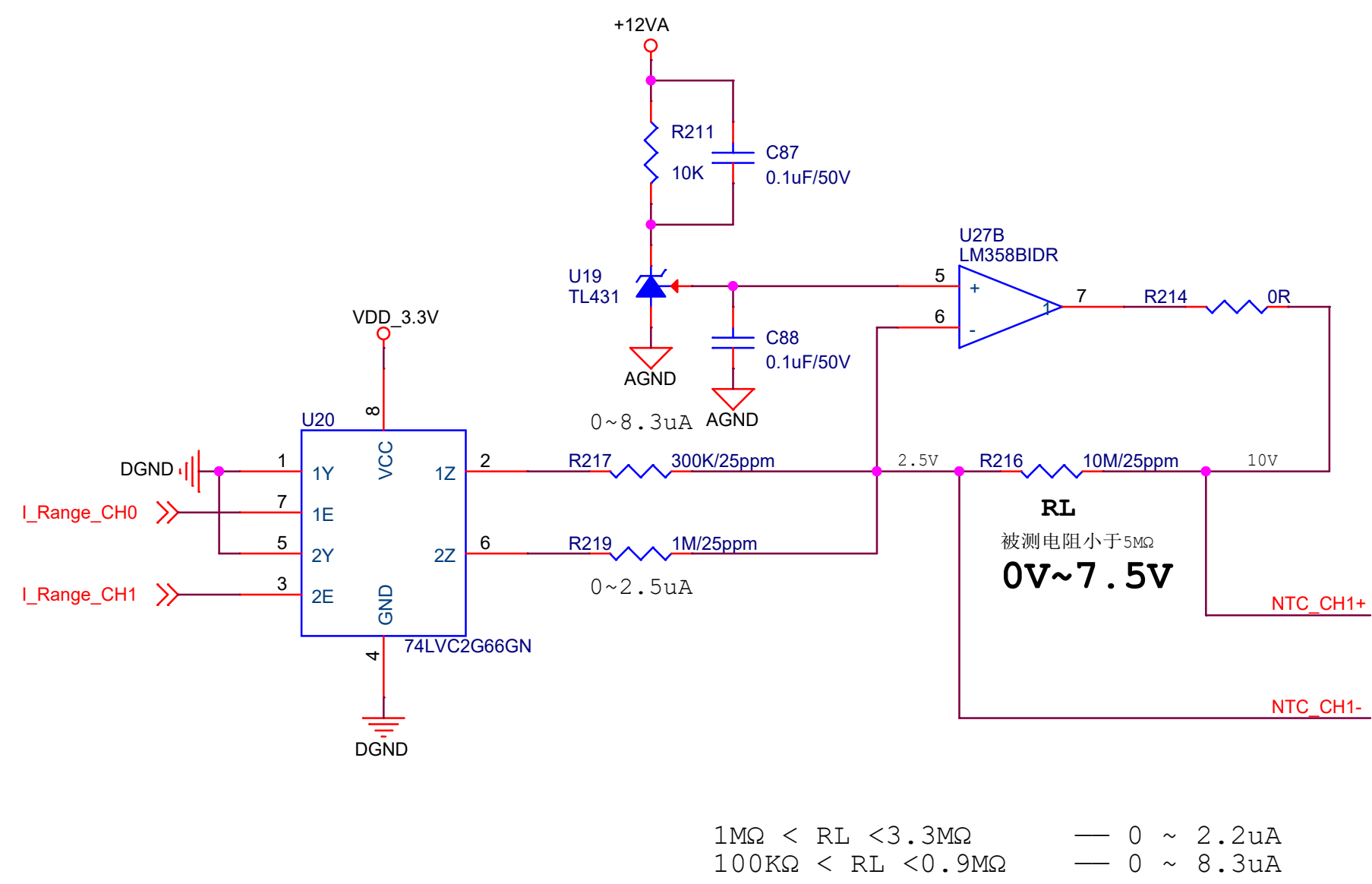
- LED_ALARM
- USART_TX1
- LED_STA
- JTAG_TDI_TOP
- JTAG_TCK_TOP
- SCL
- FE_RX_P
- FE_RX_N
- LED_RUN
- USART_RX1
- LED_LNK
- JTAG_TMS_TOP
- JTAG_TCK_TOP
- SDA
- FE_TX_P
- FE_TX_N

The connector is labeled IDC2*13P_2p54mm.



NOR Flash





$1\text{M}\Omega < R_L < 3.3\text{M}\Omega$ — 0 ~ 2.2uA
 $100\text{K}\Omega < R_L < 0.9\text{M}\Omega$ — 0 ~ 8.3uA

