



ICE Memory Modules

[TRACE32 Online Help](#)

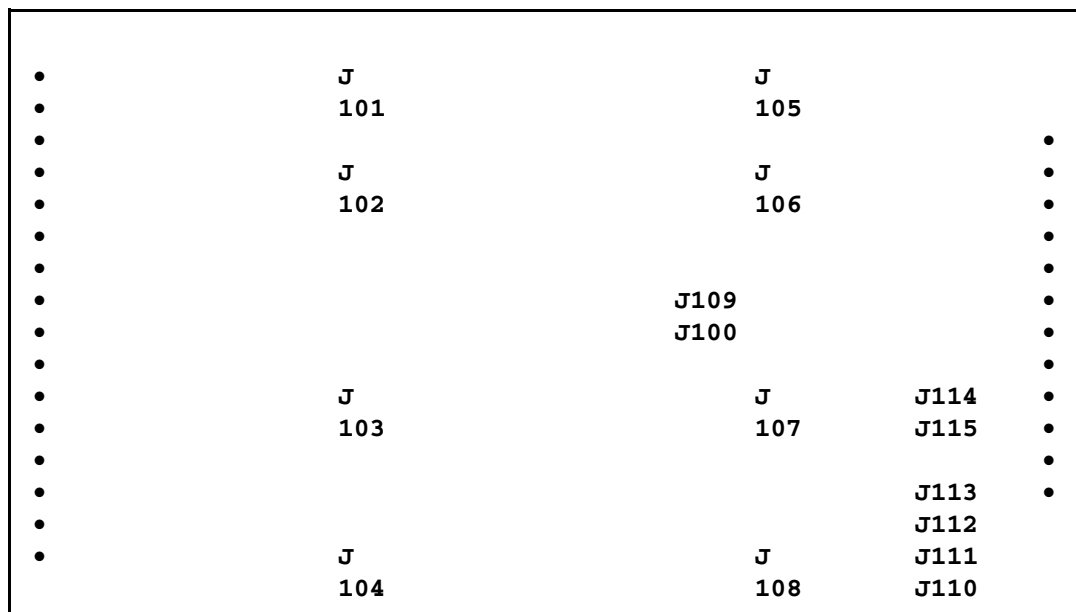
[TRACE32 Directory](#)

[TRACE32 Index](#)

TRACE32 Documents	
ICE In-Circuit Emulator	
ICE Memory Modules	1
SDIL Module	2
SRAM Module	3
DRAM Module 1/4 MByte	8
DRAM Module 4/16 MByte	10

ICE Memory Modules

Version 18-Nov-2020

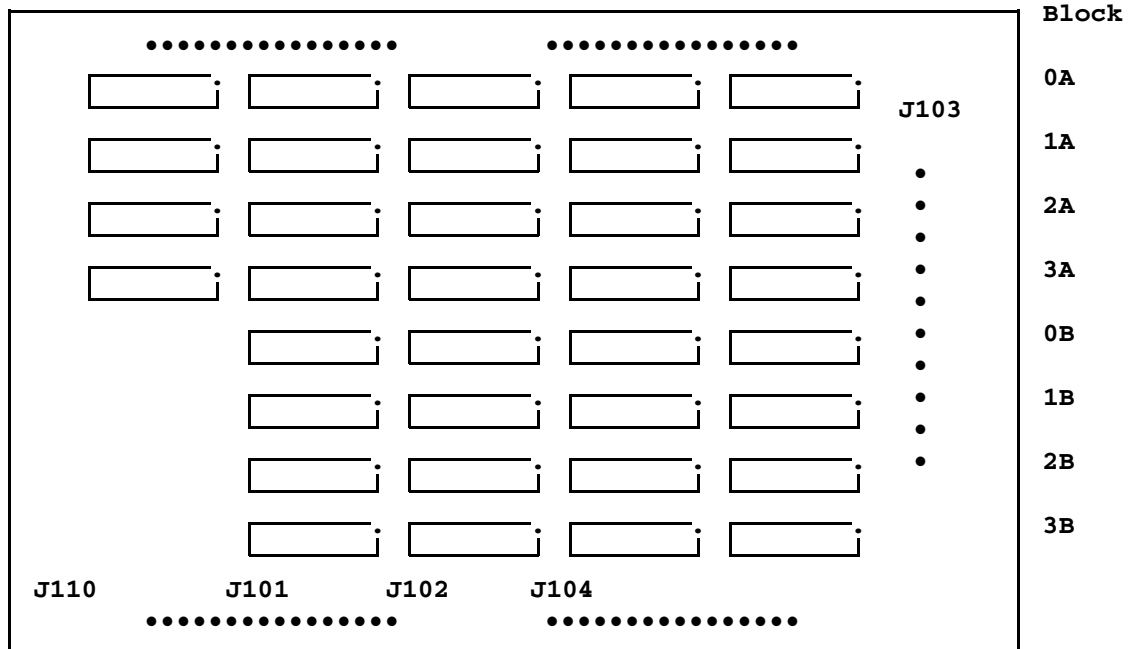


Mode A: SDIL or SDIL together with SRAM (2 MByte max.)

Mode B: 2nd SDIL modul

Jumper position

Size	128K	128K	512K	512K	2M	2M
Speed(ns)	70	70	70	70	70	70
Mode	A	B	A	B	A	B
J100	-	-	-	-	+	+
J101	+	+	+	+	-	-
J102	+	+	+	+	-	-
J103	+	+	+	+	-	-
J104	+	+	+	+	-	-
J105	+	+	+	+	-	-
J106	+	+	+	+	-	-
J107	+	+	+	+	-	-
J108	+	+	+	+	-	-
J109	+	+	+	+	-	-
J110	+	-	+	-	-	+
J111	-	+	-	+	-	-
J112	-	-	-	-	+	-
J113	-	-	-	-	-	-
J114	+	+	+	+	-	-
J115	-	-	-	-	+	+




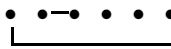
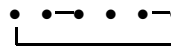

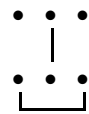

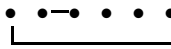
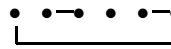

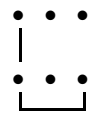

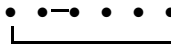
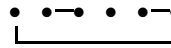

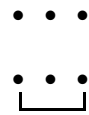




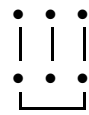




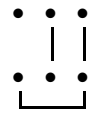




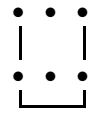




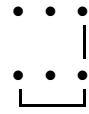




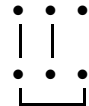
Mode A: 1st modul of SRAM, no DRAM, SDIL allowed if SRAM max. 2MByte

Mode B: 2nd Modul of SRAM, no DRAM, or SRAM together with ECC8 RAM

Mode C: Only one modul SRAM, no SDIL, DRAM allowed

RAM Mode	Type	Block	J110	J101	J102	J104	J103
512K	A	128K*8 0					
1M	A	128K*8 0-1					
2M	A	128K*8 0-3					
2M	A	128K*8 0-3 25ns					
2M	A	512K*8 0					
4M	A	512K*8 0-1					

RAM Mode	Type	Block	J110	J101	J102	J104	J103
8M	A	512K*8 0-3					
8M	A	512K*8 0-3					
25ns							
512K	B	128K*8 0					
1M	B	128K*8 0-1					
2M	B	128K*8 0-3					
2M	B	128K*8 0-3					
25ns							
2M	B	512K*8 0					

RAM	Mode	Type	Block	J110	J101	J102	J104	J103
4M	B	512K*8	0-1					
8M	B	512K*8	0-3					
8M	B	512K*8	0-3					
512K	C	128K*8	0					
1M	C	128K*8	0-1					
2M	C	128K*8	0-3					
2M	C	128K*8	0-3					
2M	C	512K*8	0					

RAM	Mode	Type	Block	J110	J101	J102	J104	J103
4M	C	512K*8	0-1					
8M	C	512K*8	0-3					
8M	C	512K*8	0-3					
		25ns						

Please note: The DRAM 16+16 MByte may be used together with SRAM or SDIL. The max. usable DRAM is reduced to 8 MByte.